



#5/B
CC
9-21/02

PATENT
ATTY. DOCKET NO. IBM/67DV1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Galbraith et al. Art Unit: 2186
Serial No. : 09/935,939 Examiner: B. R. Peugh
Filed : August 23, 2001
For : ADVANCED READ CACHE MANAGEMENT

Assistant Commissioner of Patents
Washington, DC 20231

RECEIVED

SEP 20 2002

Technology Center 2100

RESPONSE TO OFFICE ACTION

Clean Version of Amendments

Page 1, after the title, please insert this paragraph:

--This application is a continuation of U.S. Patent

Application Serial No. 09/250,349, filed February 16, 1999, the
entirety of which is incorporated herein by reference.--

Please amend claims 21-23 to read:

21. A method of caching data for a direct access
storage device having a plurality of addressable locations,
comprising the steps of:
storing, in a cache memory having a total available
memory capacity, copies of data retrieved from said direct access

09/18/2002 HWJONG1 00000108 09935939

01 FC:103

54.00 DP

I hereby certify that this correspondence is being deposited
with the United States Postal Service as first class mail,
postage prepaid in an envelope addressed to: Assistant
Commissioner of Patents, Washington, D.C. 20231 on:
September 12, 2002

Thomas W. Humphrey
Reg. No. 34,353